

# CONDUCTIVE POLYMER CHIP CAPACITORS

## - PL SERIES -

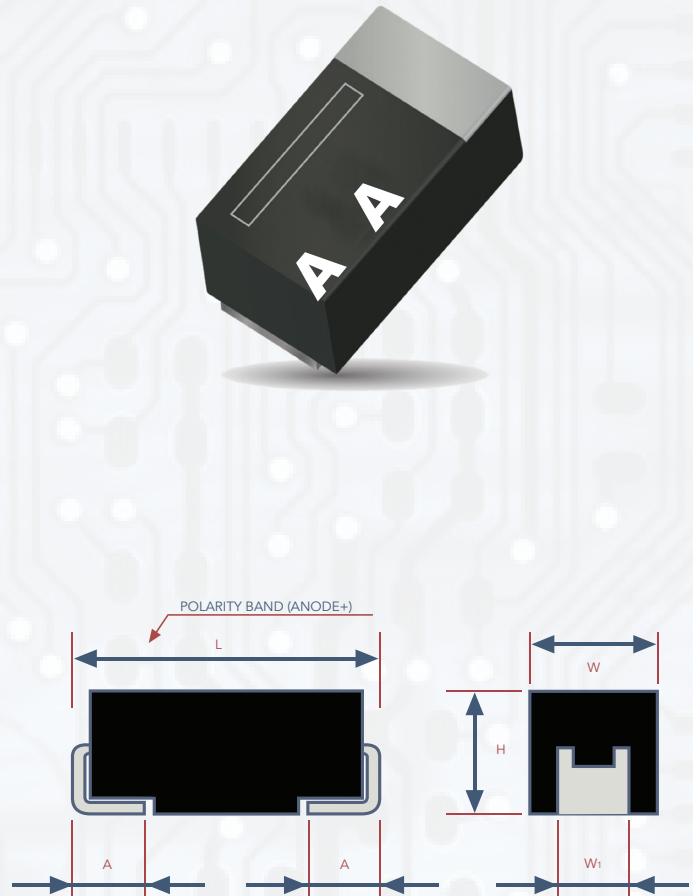
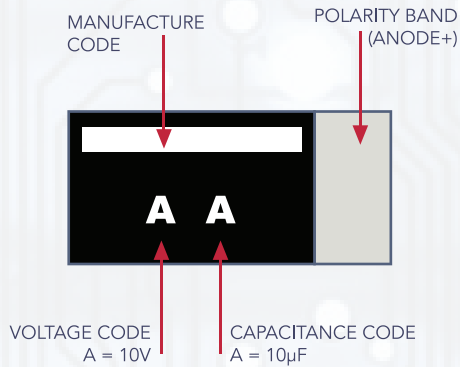
### FEATURES

- Ta-polymer technology
- High ripple capability
- High CV
- Surge robust
- Undertab LF
- Smaller Case Size

### APPLICATIONS

- For high component density PCB design like mobile gaming, computer card
- IoT
- SSD
- Sensors

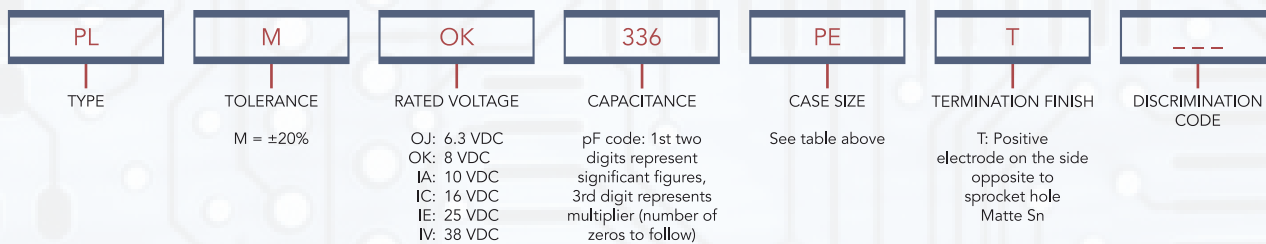
### MARKING



### CASE DIMENSIONS

CODE	EIA CODE	EIA METRIC	L±0.20 (0.008)	W±0.20 (0.008)	H MAX	W <sub>1</sub> ±0.20 (0.008)	A ± 0.10 (0.004)
M	0603	1608-10	1.60+0.20-0.00 (0.063+0.008-0.000)	0.85±0.10 (0.033±0.004)	0.80+0.20-0.00 (0.031+0.008-0.000)	0.55±0.10 (0.022±0.004)	0.50 (0.020)
PE	0805	2012-08	2.00 (0.079)	1.25 (0.049)	0.80 (0.031)	0.85 (0.033)	0.50 (0.020)
PL	0805	2012-10	2.00 (0.079)	1.25 (0.049)	0.90±0.10 (0.035±0.004)	0.85 (0.033)	0.50 (0.020)
PS	0805	2012-09	2.00 (0.079)	1.25 (0.049)	0.90 (0.035)	0.85 (0.033)	0.50 (0.020)

### PART NUMBERING



## TECHNICAL SPECIFICATIONS

TECHNICAL DATA	All technical data relate to an ambient temperature of +25°C
CAPACITANCE RANGE	1.0µF to 47µF
CAPACITANCE TOLERANCE	±20%
LEAKAGE CURRENT DCL	Please see the ratings and part number reference table below
TEMPERATURE RANGE	-55°C to +105°C

Note: Conductive Polymer Capacitors are designed to operate within the limits of the environmental conditions specified for each series. If operated continuously at their maximum temperature and / or humidity limit, or beyond these limits, capacitors may exhibit a parametric shift in capacitance and increases in ESR. These changes may occur earlier if the specified environmental conditions are exceeded. Similarly, their normal operational time period will be significantly extended if their general duty cycle includes operation below maximum temperature within humidity controlled environments. Careful attention should be paid to maximum temperature with associated high humidity environments as well as voltage derating, ripple current and current surges.

## CAPACITANCE AND RATED VOLTAGE RANGE

- Letter denotes case size

CAPACITANCE		RATED VOLTAGE DC (VR) @85°C							CAP CODE
UF	CODE	6.3V (J)	8V (k)	10V (A)	16V (C)	25V (E)	38V (V)		
1.0	105						500 (PS)	A	
2.2	225							J	
4.7	475					300, 500 (PL), 500 (PL), 500 (PS)		S	
10	106			300(M)	150 (PL)			A	
22	226	200,300(M)		200(PL)				J	
33	336		150(PE)					N	
47	476	200, 300(M), 150, 200(PL), 150(PS)	150(PS)					S	

Released ratings, (ESR ratings in mOhms)

Note: Voltage ratings are minimum values. CAL-CHIP ELECTRONICS INC. reserves the right to supply higher ratings in the same case size, to the same reliability standards.

## RATES & PART NUMBER REFERENCE

PART NUMBER	CASE SIZE	CAP. (µF)	RATED VOLTAGE (V)	MAX OPERATING TEMP. (C)	DCL MAX (UA)	DF MAX (%)	ESR MAX. @100kHz (m Ω)	100KHZ RMS CURRENT (mA) 45°C	MSL
<b>6.3 VOLT</b>									
PLMOJ226MT-ZD1	M	22	6.3	105	13.9	15	200	418	3
PLMOJ226MT	M	22	6.3	105	13.9	15	300	341	3
PLMOJ226MT-02	M	22	6.3	105	13.9	15	300	341	3
PLMOJ226MT-029	M	22	6.3	105	13.9	15	300	341	3
PLMOJ476MT-CD1	M	47	6.3	105	5.0	15	200	418	3
PLMOJ476MT-CM1	M	47	6.3	105	5.0	15	300	341	3
PLMOJ476MT-ZM1	M	47	6.3	105	29.7	15	300	341	3
PLMOJ476PLT-CF1	PL	47	6.3	105	14.8	15	150	516	3
PLMOJ476PLT-ZCT	PL	47	6.3	105	29.7	15	150	516	3
PLMOJ476PLT-ZF1	PL	47	6.3	105	29.7	15	150	516	3
PLMOJ476PLT-ZF9	PL	47	6.3	105	29.7	15	150	516	3
PLMOJ476PST-ZD1	PL	47	6.3	105	29.7	15	200	447	3
PLMOJ476PST-ZF1	PS	47	6.3	105	29.7	15	150	516	3
PLMOJ476PST-ZF9	PS	47	6.3	105	29.7	15	150	516	3
<b>8 VOLT</b>									
PLMOK336PET-ZF1	PE	33	8	105	26.4	15	150	516	3
PLMOK476PST-ZF1	PS	47	8	105	37.6	15	150	516	3
PLMOK476PST-ZF9	PS	47	8	105	37.6	15	150	516	3
<b>10 VOLT</b>									
PLMIA106MT-ZM1	M	10	10	105	10.0	15	300	341	3
PLMIA226PLT	PL	22	10	105	22.0	15	200	447	3
<b>16 VOLT</b>									
PLMIC106PLT-ZF1	PL	10	16	105	48.0	10	150	516	3
<b>25 VOLT</b>									
PLMIE475PLT-ZM1	PL	4.7	25	105	11.8	10	300	365	3
PLMIE475PLT-ZT1	PL	4.7	25	105	11.8	10	500	283	3
PLMIE475PST-ZT1	PS	4.7	25	105	11.8	10	500	282	3
<b>38 VOLT</b>									
PLMIV105PST-UT1	PS	1.0	38	105	11.4	10	500	280	3

Moisture Sensitivity Level (MSL) is defined according to J-STD-020. I All technical data relates to an ambient temperature of +25°C. Capacitance and DF are measured at 120Hz, 0.5RMS with DC bias of 1.5 volts. I DCL is measured at rated voltage after 5 minutes.

ESR allowed to move up to 1.25 times catalog limit post mounting

NOTE: CAL-CHIP reserves the rights to supply higher voltage rating in the same case size, to the same reliability standards.

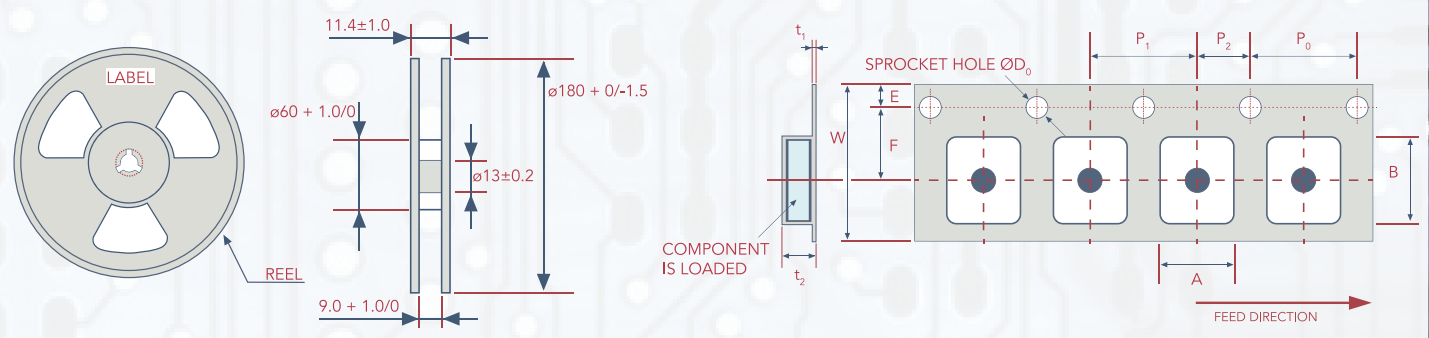


## QUALIFICATION TABLE

TEST	TCSSO SERIES (TEMPERATURE RANGE -55°C TO +105°C)					
	CONDITON			CHARACTERISTICS		
ENDURANCE	Apply rated voltage (Ur) at 85°C for 1000hrs through a serial resistance of $\leq 3.0\Omega$ . Stabilize at room temperature for 24 hours before measuring.			Visual Examination	no visible damage	
				DCL	4x initial limit	
				$\Delta C/C$	within $\pm 20\%$ of initial value	
				DF	3x initial limit	
HUMIDITY	Store at $40 \pm 2^\circ\text{C}$ , 90-95% relative humidity for 500+ 12/0 hours. Stabilize at room temperature and humidity for 24 hours before measuring.			Visual Examination	no visible damage	
				DCL	3x initial limit	
				$\Delta C/C$	within +30/-20% of initial value	
				DF	3x initial limit	
TEMPERATURE STABILITY	STEP	TEMPERATURE °C	DURATION (MIN)		-55°C	+105°C
	1	-55	15	DCL	N/A	10xIL*V
	2	+105	15	$\Delta C/C$	0/-20%	+50/0%
				DF	IL*	IL*
SURGE VOLTAGE	Apply 1.3x rated voltage (Ur) at $85 \pm 2^\circ\text{C}$ for 1000 cycles, 300sec charge and 30sec discharge resistance 1000 $\Omega$			Visual Examination	no visible damage	
				DCL	2x initial limit	
				$\Delta C/C$	$\pm 20\%$ of initial limit	
				DF	2x initial limit	
VIBRATION	4.17 JIS C 5101-1 Frequency: 10 to 55 to 10Hz/min. Amplitude: 1.5mm Time: 2hours each in X and Y directions			Visual Examination	no visible damage	
				DCL	initial limit	
				$\Delta C/C$	within $\pm 5\%$ of initial value	
				DF	initial limit	

\*Initial Limit  
For use outside of recommended conditions and special request, please contact CAL-CHIP ELECTRONICS, INC.  
Initial measurement max. 1hr after the removal from dry pack or after pretreatment at 85°C for 24 hours.

## PACKAGING SPECIFICATIONS



CASE	A $\pm 0.10$	B $\pm 0.10$	W $\pm 0.20$	E $\pm 0.10$	F $\pm 0.05$	P1 $\pm 0.10$	P2 $\pm 0.05$	P0 $\pm 0.10$	D0 $\pm 0.10/0$	T1 $\pm 0.05$	T2 $\pm 0.05$	AV (MM)
M	1.15	2.00	8.00	1.75	3.50	4.00	2.00	4.00	Ø1.50	0.20	1.10 $\pm 0.10$	3,000 PCS
PE	1.60	2.40	8.00	1.75	3.50	4.00	2.00	4.00	Ø1.50	0.25	1.05	4,000 PCS
PL	1.60	2.40	8.00	1.75	3.50	4.00	2.00	4.00	Ø1.50	0.25	1.05	3,000 PCS
PS	1.60	2.40	8.00	1.75	3.50	4.00	2.00	4.00	Ø1.50	0.25	1.05	3,000 PCS